FIG.1

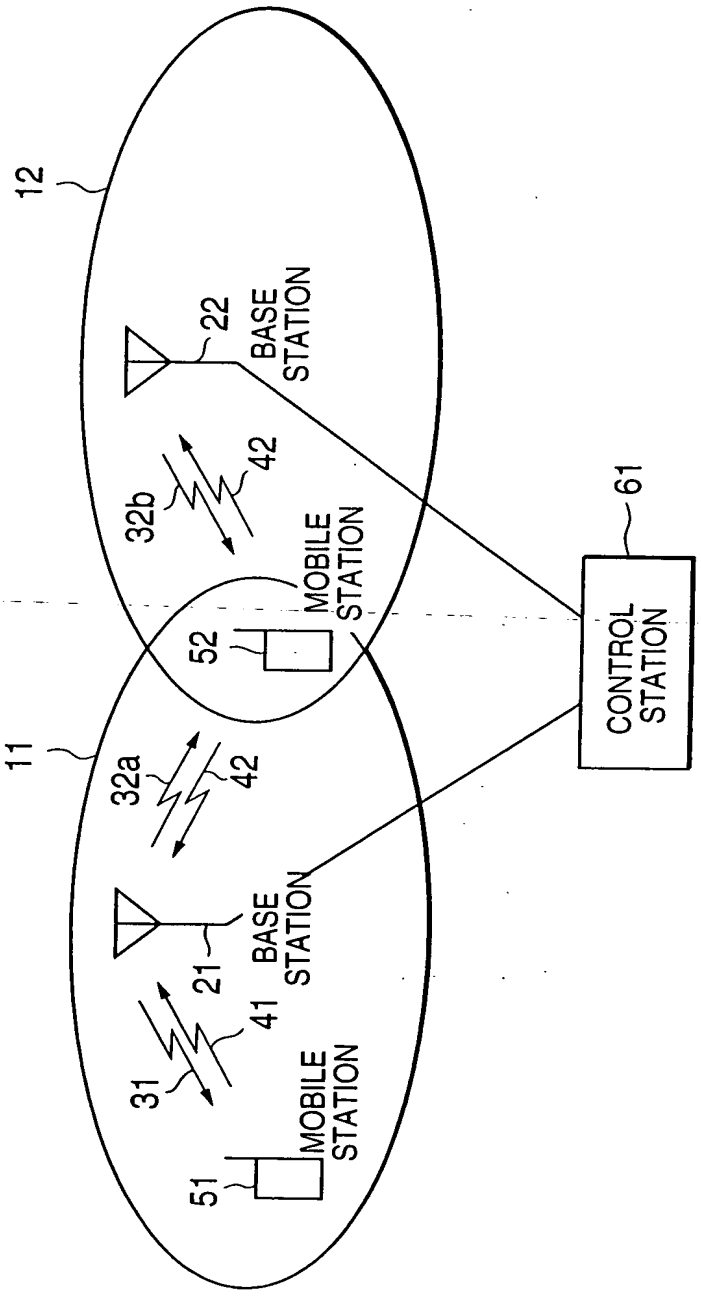

12
22
BASE STATION
32b
42
MOBILE STATION
52
61
CONTROL STATION
11
32a
42
BASE STATION
21
41
31
MOBILE STATION
51

FIG.2

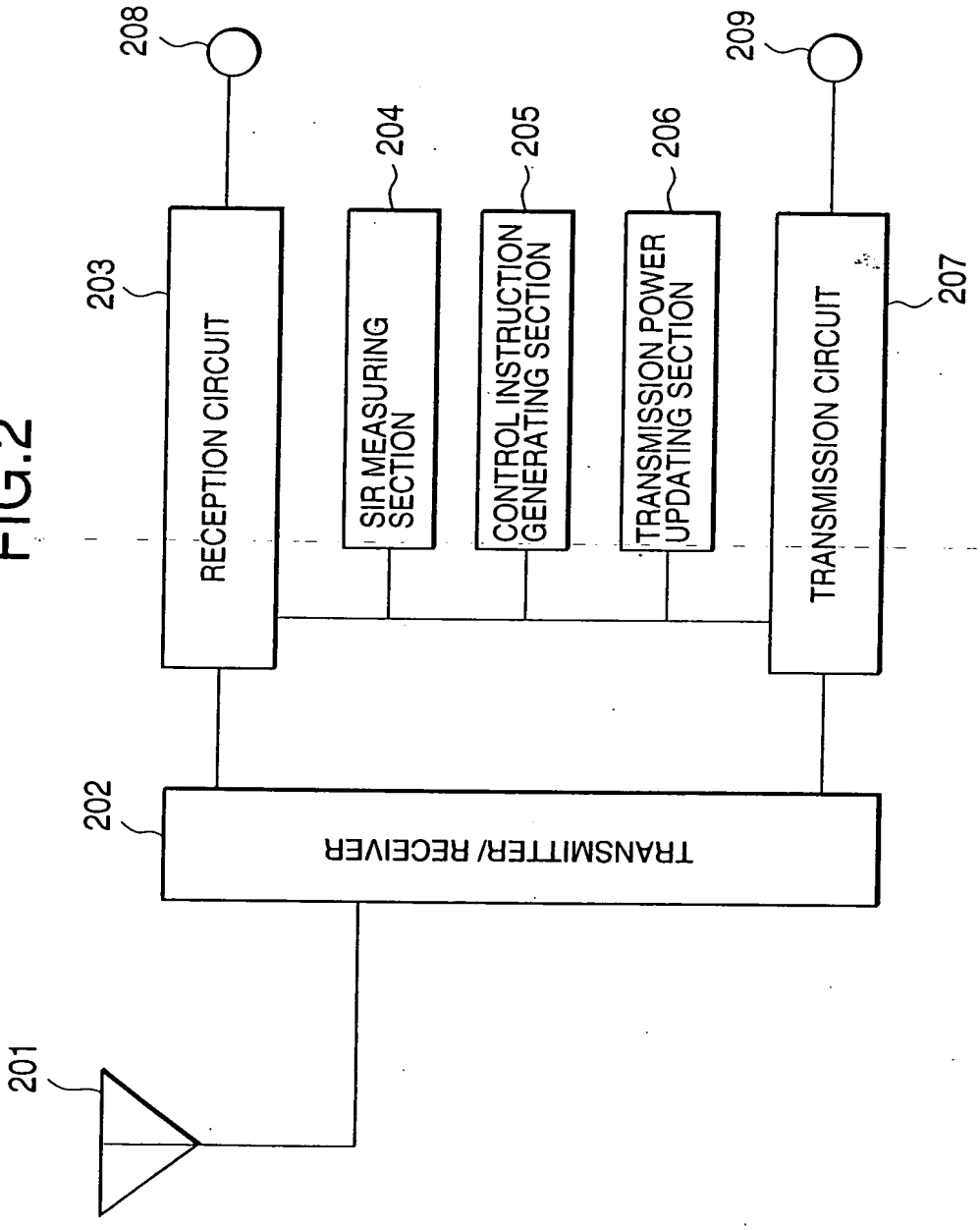

208
209
204
205
206
203
RECEPTION CIRCUIT
SIR MEASURING SECTION
CONTROL INSTRUCTION GENERATING SECTION
TRANSMISSION POWER UPDATING SECTION
TRANSMISSION CIRCUIT
207
202
TRANSMITTER/ RECEIVER
201

FIG.3

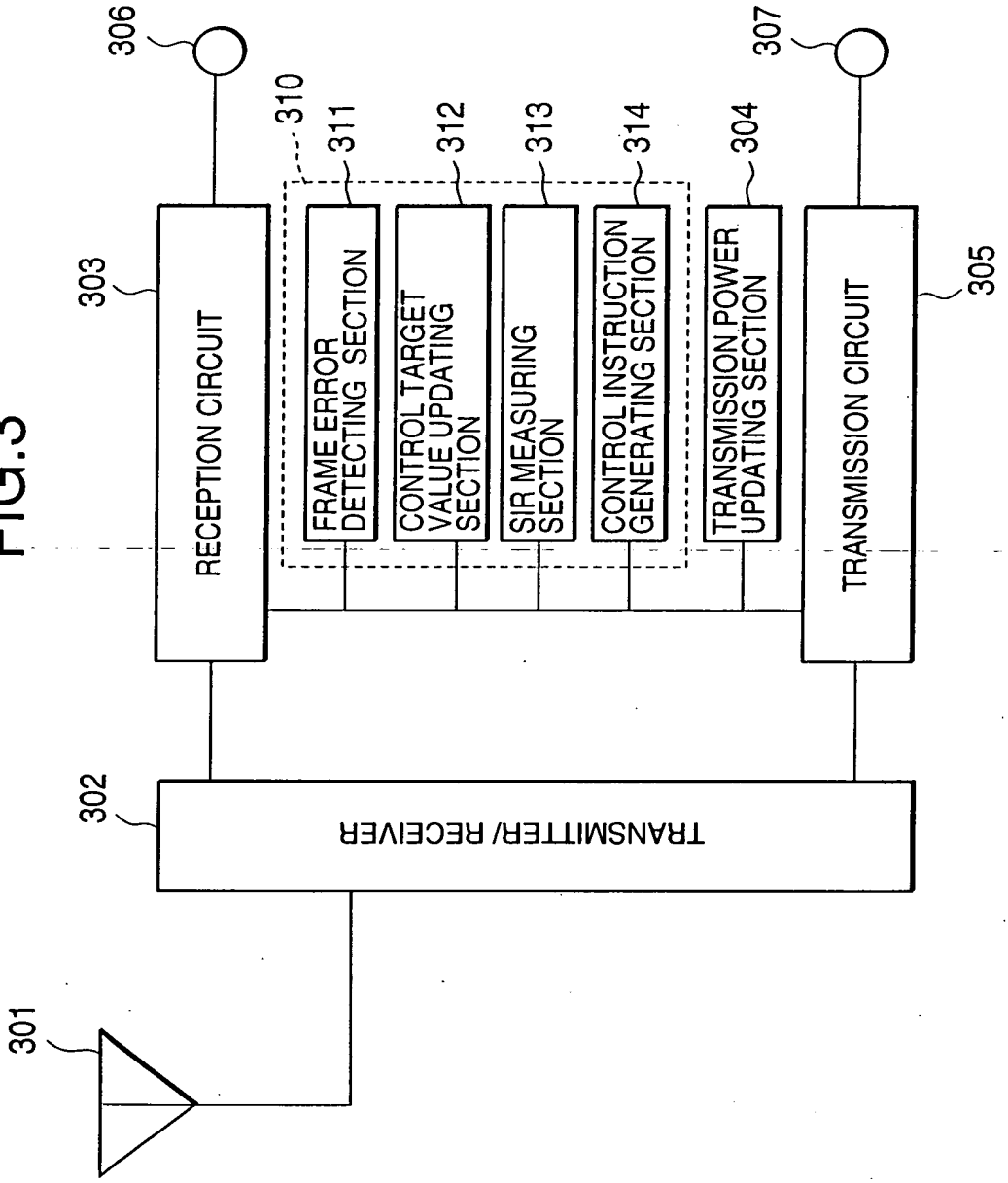

306
307
310
311
312
313
314
304
303
305
RECEPTION CIRCUIT
FRAME ERROR DETECTING SECTION
CONTROL TARGET VALUE UPDATING SECTION
SIR MEASURING SECTION
CONTROL INSTRUCTION GENERATING SECTION
TRANSMISSION POWER UPDATING SECTION
TRANSMISSION CIRCUIT
302
TRANSMITTER/ RECEIVER
301

FIG.4

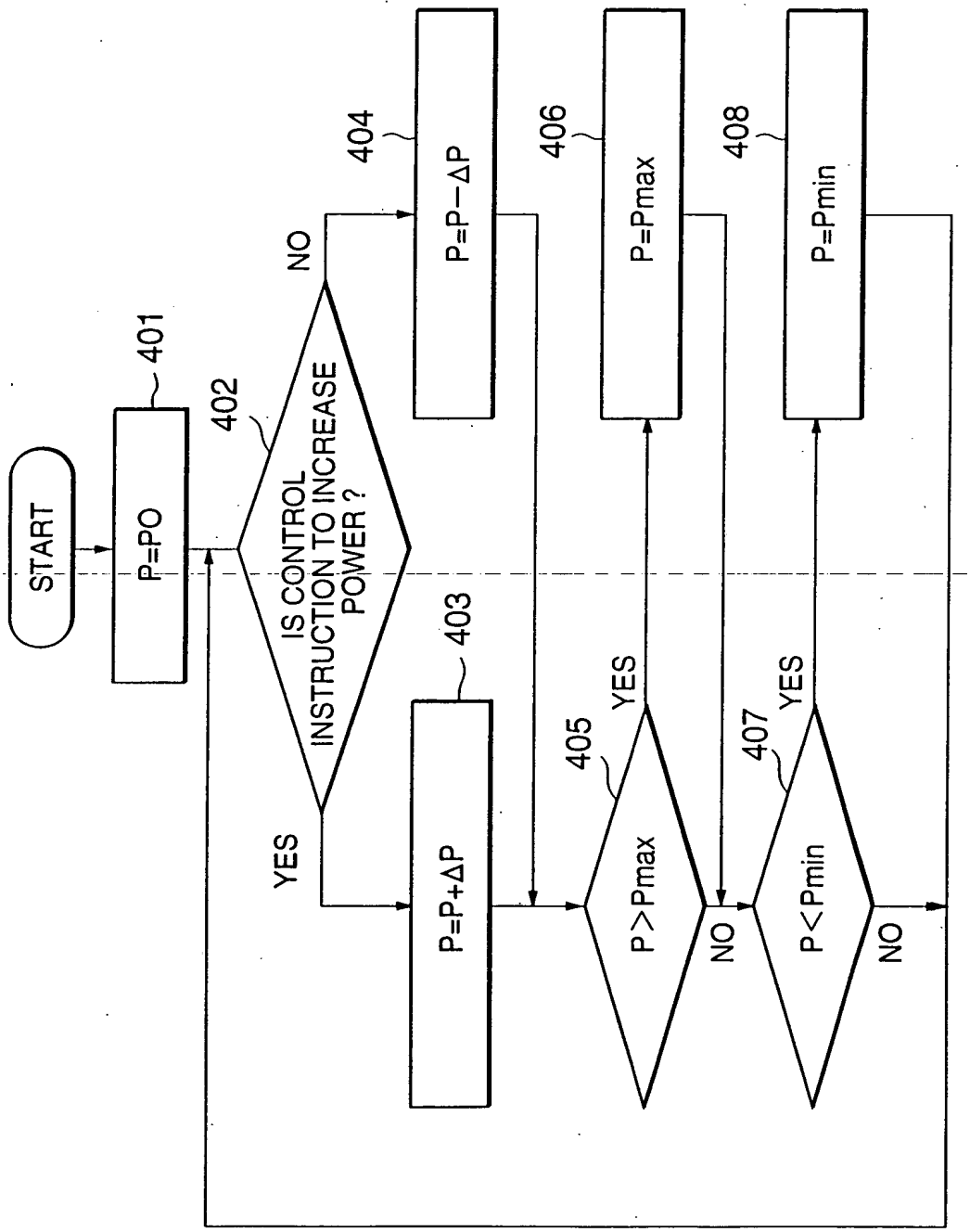

START
P=PO
401
IS CONTROL INSTRUCTION TO INCREASE POWER ?
402
NO
YES
P=P+ΔP
403
P=P−ΔP
404
P>Pmax
405
YES
NO
P=Pmax
406
P<Pmin
407
YES
NO
P=Pmin
408

FIG.5

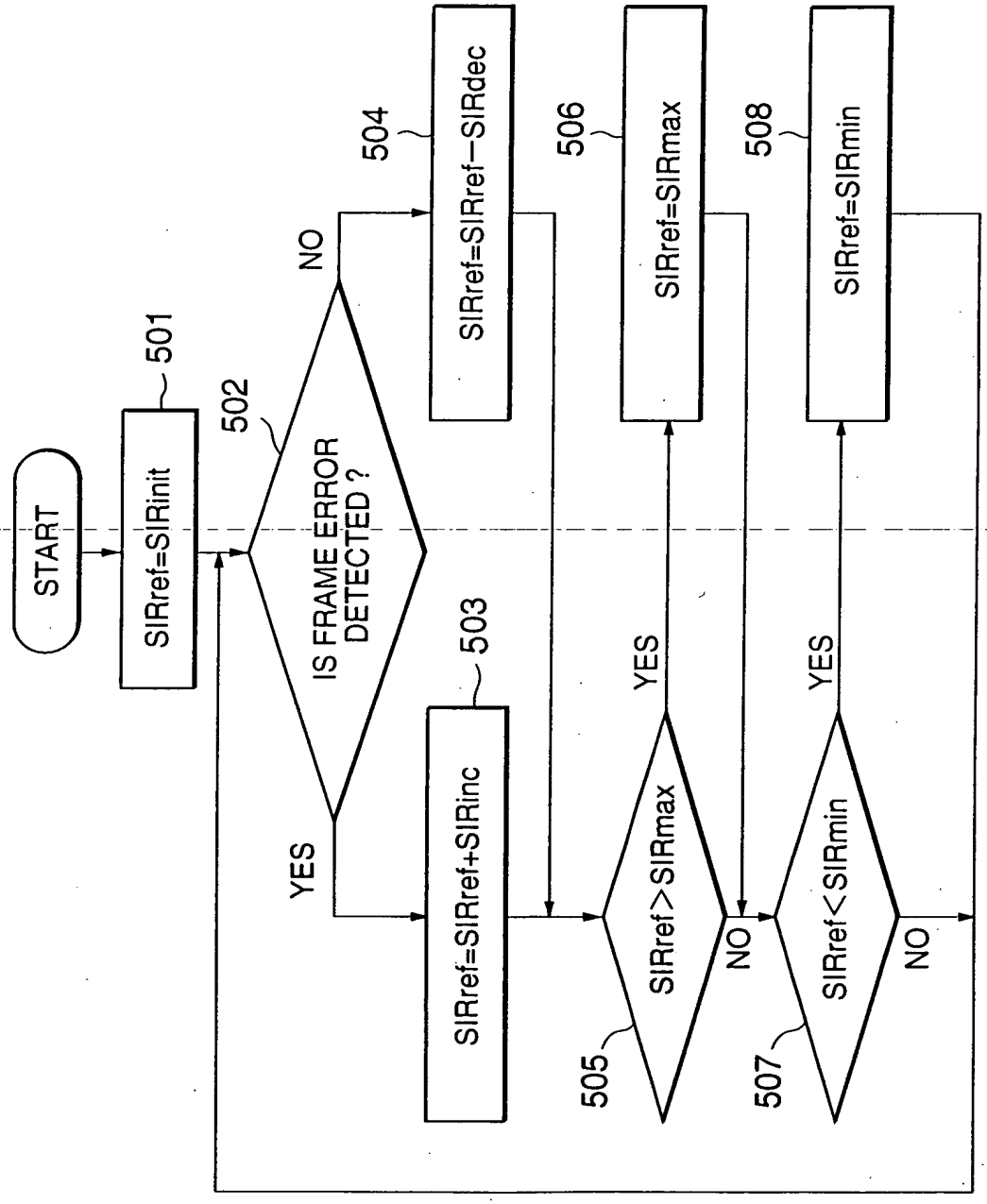

START
SIRref=SIRinit
501
502
IS FRAME ERROR DETECTED ?
NO
YES
504
SIRref=SIRref−SIRdec
503
SIRref=SIRref+SIRinc
505
SIRref>SIRmax
NO
YES
506
SIRref=SIRmax
507
SIRref<SIRmin
NO
YES
508
SIRref=SIRmin

FIG.6

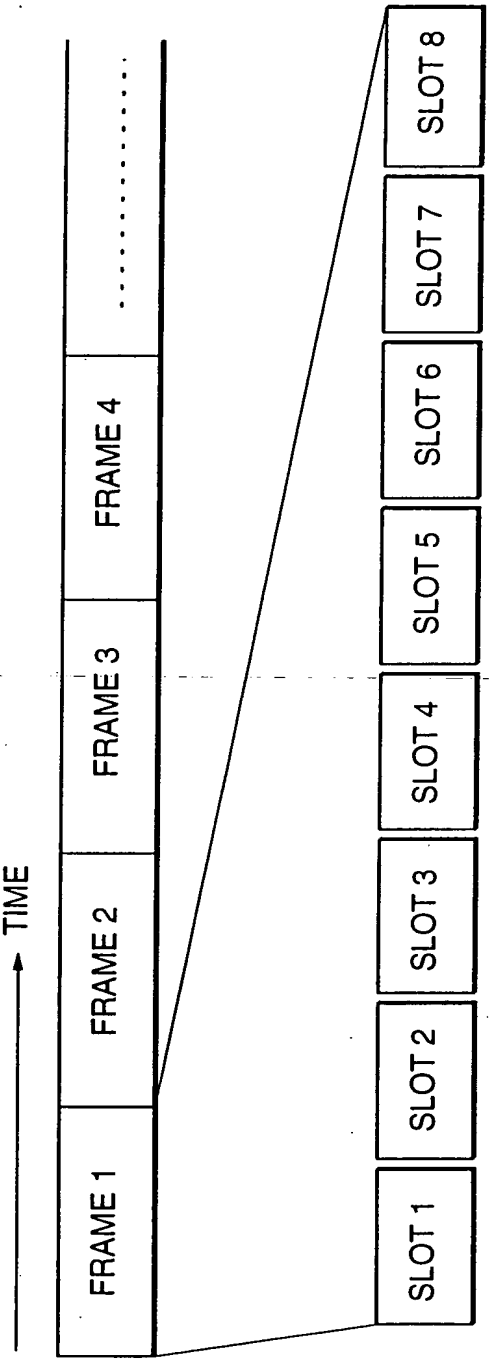

TIME
FRAME 1
FRAME 2
FRAME 3
FRAME 4
SLOT 1
SLOT 2
SLOT 3
SLOT 4
SLOT 5
SLOT 6
SLOT 7
SLOT 8

FIG.7

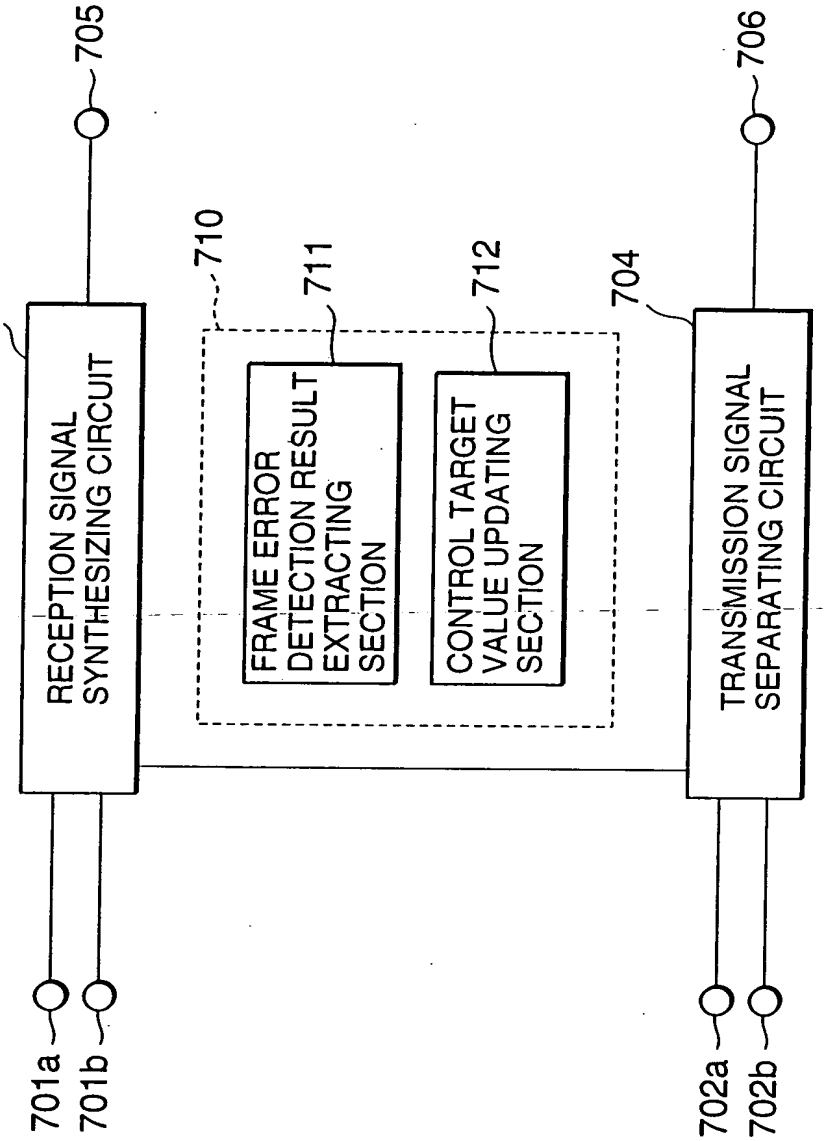
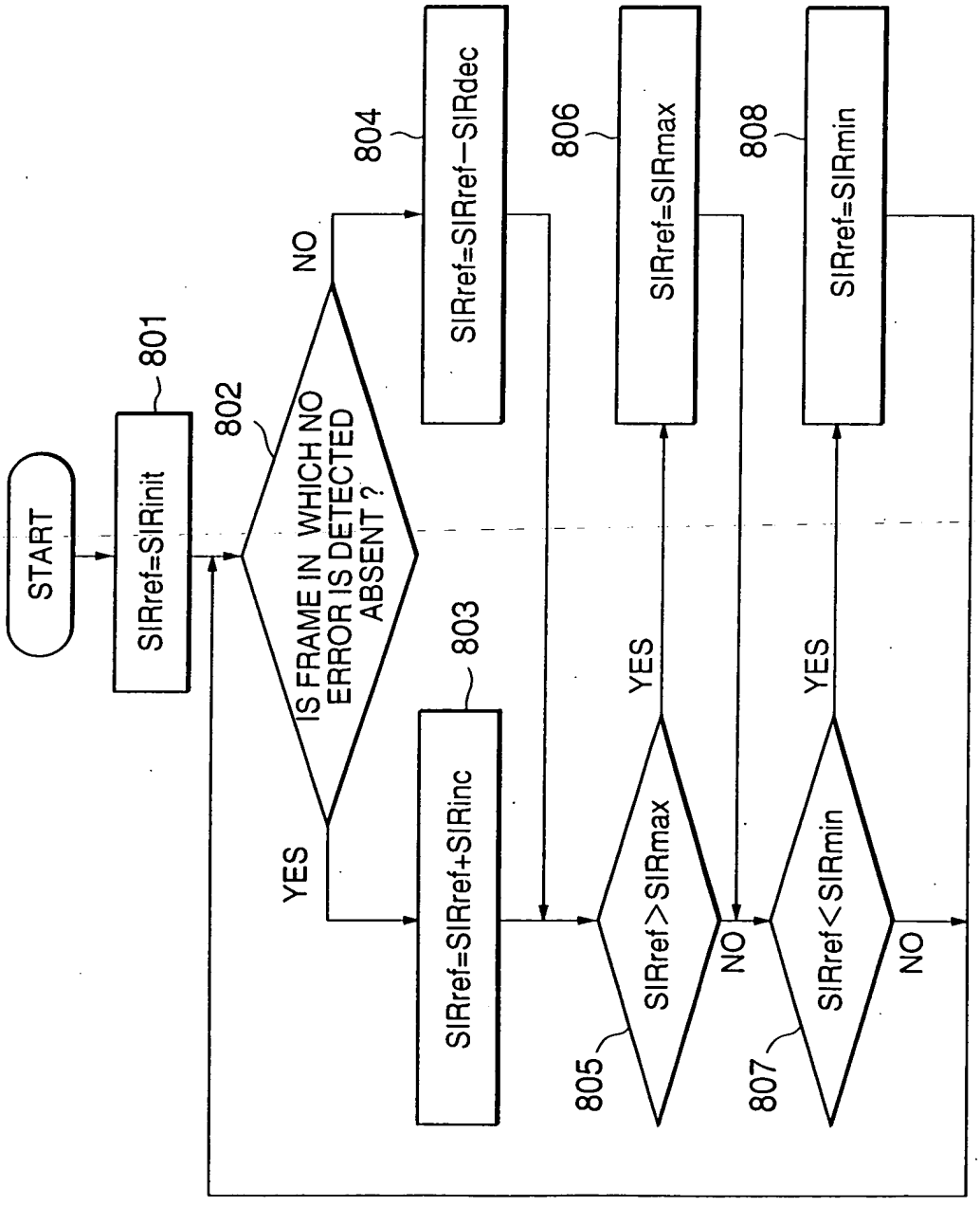

705
706
703
710
711
712
704
RECEPTION SIGNAL SYNTHESIZING CIRCUIT
FRAME ERROR DETECTION RESULT EXTRACTING SECTION
CONTROL TARGET VALUE UPDATING SECTION
TRANSMISSION SIGNAL SEPARATING CIRCUIT
701a
701b
702a
702b

FIG.8


START
SIRref=SIRinit
801
IS FRAME IN WHICH NO ERROR IS DETECTED ABSENT ?
802
NO
YES
SIRref=SIRref+SIRinc
803
SIRref=SIRref−SIRdec
804
SIRref>SIRmax
805
NO
YES
SIRref=SIRmax
806
SIRref<SIRmin
807
NO
YES
SIRref=SIRmin
808